AVAQ

UART 2-CH 64byte FIFO 3.3V 48-Pin LQFP T/R

Manufacturer:	Texas Instruments, Inc
Package/Case:	LQFP-48
Product Type:	Drivers
RoHS:	RoHS Compliant/Lead free ![RoHS]
Lifecycle:	Active
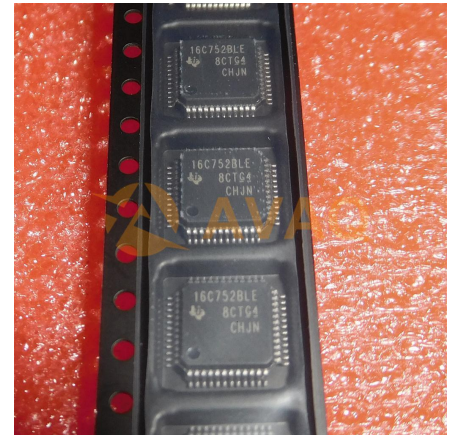
Images are for reference only

Inquiry

General Description

The TL16C752B is a dual-universal asynchronous receiver/transmitter (UART) with 64-byte FIFOs, automatic hardware/software flow control, and data rates up to 3 Mbps. The TL16C752B offers enhanced features. It has a transmission control register (TCR) that stores receiver FIFO threshold levels to start/stop transmission during hardware and software flow control. With the FIFO RDY register, the software gets the status of TXRDY/RXRDY for all four ports in one access. On-chip status registers provide the user with error indications, operational status, and modem interface control. System interrupts may be tailored to meet user requirements. An internal loopback capability allows onboard diagnostics.The UART transmits data, sent to it over the peripheral 8-bit bus, on the TX signal and receives characters on the RX signal. Characters can be programmed to be 5, 6, 7, or 8 bits. The UART has a 64-byte receive FIFO and transmit FIFO and can be programmed to interrupt at different trigger levels. The UART generates its own desired baud rate based upon a programmable divisor and its input clock. It can transmit even, odd, or no parity and 1, 1.5, or 2 stop bits. The receiver can detect break, idle, or framing errors, FIFO overflow, and parity errors. The transmitter can detect FIFO underflow. The UART also contains a software interface for modem control operations, and has software flow control and hardware flow control capabilities.
The TL16C752B is available in a 48-pin PT (LQFP) package.

Key Features

Controlled Baseline

One Assembly Site

Test Site

One Fabrication Site

Extended Temperature Performance of\ –55°C to 110°C and –40°C to 105°C

Enhanced Diminishing Manufacturing Sources (DMS) Support

Enhanced Product Change Notification

Qualification Pedigree(1)

Pin Compatible With ST16C2550 With Additional Enhancements

Up to 1.5-Mbps Baud Rate When Using Crystal (24-MHz Input Clock)

Up to 3-Mbps Baud Rate When Using Oscillator or Clock Source (48-MHz Input Clock)

64-Byte Transmit FIFO

64-Byte Receive FIFO With Error Flags

Programmable and Selectable Transmit and Receive FIFO Trigger Levels for DMA and Interrupt Generation

Programmable Receive FIFO Trigger Levels for Software/Hardware Flow Control

Software/Hardware Flow Control

Programmable Xon/Xoff Characters

Programmable Auto-CD)

Recommended For You

AVAQ SEMICONDUCTOR CO., LIMITED   <u>**Email: sales@avaq.com**</u>
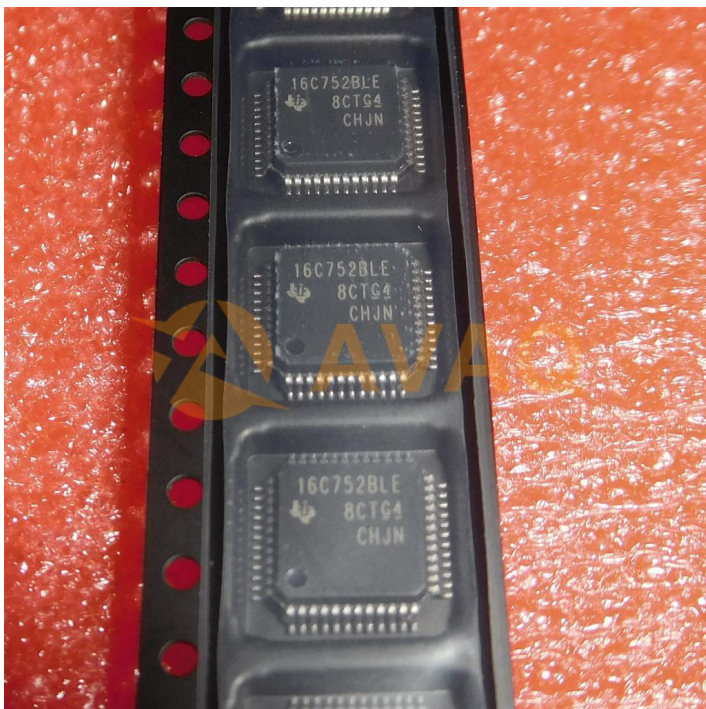
TLV320AIC23BIPWR

Texas Instruments, Inc

TSSOP28

TLV320AIC3104IRHBR

Texas Instruments, Inc

QFN32

TL16C554AIPN

Texas Instruments, Inc

LQFP80

TLV320AIC3101IRHBR

Texas Instruments, Inc

QFN32

TL16C554APN

Texas Instruments, Inc

LQFP80

TLV320AIC24KIPFBR

Texas Instruments, Inc

TQFP-48

TL16C554PN

Texas Instruments, Inc

QFP

TLV320AIC24KIPFB

Texas Instruments, Inc

TQFP-48

TL16C550DIPFBR

Texas Instruments, Inc

48-TQFP

TLC320AC01CFN

Texas Instruments, Inc

PLCC28

TL16C552AFN

Texas Instruments, Inc

PLCC

TL16C450FN

Texas Instruments, Inc

PLCC44

TL16C554FN

Texas Instruments, Inc

PLCC

TLV320AIC31IRHBR

Texas Instruments, Inc

VQFN32

TLV320AIC3100IRHBR

Texas Instruments, Inc

QFN32