Digital Audio Interface Transceiver 1TX 1RX 192Ksps 20-Pin SSOP T/R

Manufacturer:	Cirrus Logic, Inc
Package/Case:	SSOP20
Product Type:	Discrete Semiconductor Modules
RoHS:	RoHS Compliant/Lead free RoHS
Lifecycle:	Active
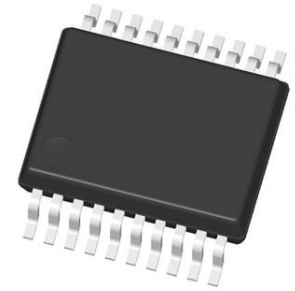
Images are for reference only

Inquiry

General Description

The WM8804 is a high performance consumer mode S/PDIF transceiver with support for 1 received channel and 1 transmitted channel. A crystal derived, or externally provided high quality master clock is used to allow low jitter recovery of S/PDIF supplied master clocks. Generation of all typically used audio clocks is possible using the high performance internal PLL. A dedicated CLKOUT pin provides a high drive clock output. A pass through option is provided which allows the device simply to be used to clean up (de-jitter) the received digital audio signals. The device may be used under software control or stand alone hardware control modes. In software control mode, both two-wire with read back and three-wire interface modes are supported. Status and error monitoring is built-in and results can be read back over the control interface, on the GPO pins or streamed over the audio data interface in 'With Flags' mode (audio data with status flags appended). The audio data interface supports I²S, left justified, right justified and DSP audio formats of 16-24 bit word length, with sample rates from 32 to 192ks/s. The device is supplied in a 20-lead Pb-free SSOP package.

Key Features

S/PDIF (IEC60958-3) compliant

Advanced jitter attenuating PLL with low intrinsic period jitter of 50 ps RMS

S/PDIF recovered clock using PLL, or stand alone crystal derived clock generation

Supports 10 – 27MHz crystal clock frequencies

Two-wire / three-wire serial or hardware control interface

Programmable audio data interface modes:

I²S, Left, Right Justified or DSP

16/20/24 bit word lengths

1 channel receiver input and 1 channel transmit output

Auto frequency detection / synchronisation

Selectable output status data bits

Up to 3 configurable GPO pins

De-emphasis flag output

Non-audio detection including DOLBYTM and DTSTM

Channel status changed flag

Configurable clock distribution with selectable output MCLK rate of 512fs, 256fs, 128fs and 64fs

2.7 to 3.6V digital and PLL supply voltages

20-lead SSOP package

Recommended For You

WM8281ECS/R	**WM8804GEDS/V**	**WM8805GEDS/V**
Cirrus Logic, Inc	Cirrus Logic, Inc	Cirrus Logic, Inc
W-CSP138	SSOP20	20
CS8406-CSZ	**CS44800-CQZR**	**CS8416-DZZ**
Cirrus Logic, Inc	Cirrus Logic, Inc	Cirrus Logic, Inc
SOP28	LQFP	TSSOP
CS8416-CZZ	**CS8416-CSZ**	**CS8405A-CS**
Cirrus Logic, Inc	Cirrus Logic, Inc	Cirrus Logic, Inc
TSSOP28	SOP28	SOP

CS495313-CVZ

Cirrus Logic, Inc

QFP

CS8406-DZZ

Cirrus Logic, Inc

TSSOP28

CS8420-DSZ

Cirrus Logic, Inc

SOP28

CS8415A-CZZ

Cirrus Logic, Inc

TSSOP28

CS3318-CQZ

Cirrus Logic, Inc

LQFP48

CS44600-CQZ

Cirrus Logic, Inc

QFP