

# **SN74LS123N**

# Monostable Multivibrator Dual-Element 0°C 70°C 16-Pin PDIP Tube

| Manufacturer: | Texas Instruments, Inc        |
|---------------|-------------------------------|
| Package/Case: | DIP16                         |
| Product Type: | Logic ICs                     |
| RoHS:         | RoHS Compliant/Lead free RoHS |
| Lifecycle:    | Active                        |



Images are for reference only

Inquiry

### **General Description**

These d-c triggered multivibrators feature output pulse-duration control by three methods. The basic pulse time is programmed by selection of external

resistance and capacitance values (see typical application data). The '122 and 'LS122 have internal timing resistors that allow the circuits to be used with only an external capacitor, if so desired. Once triggered, the basic pulse duration may be extended by retriggering the gated low-level-active (A) or high-level-active (B) inputs, or be reduced by use of the overriding clear. Figure 1 illustrates pulse control by retriggering and early clear.

The 'LS122 and 'LS123 are provided enough Schmitt hysteresis to ensure jitter-free triggering from the B input with transition rates as slow as 0.1 millivolt per nanosecond.

The Rint is nominally 10 k for '122 and 'LS122.

### **Key Features**

D-C Triggered from Active-High or Active-Low Gated Logic Inputs

Retriggerable for Very Long Output Pulses, Up to 100% Duty Cycle

Overriding Clear Terminates Output Pulse

'122 and 'LS122 Have Internal Timing Resistors

#### Description

These d-c triggered multivibrators feature output pulse-duration control by three methods. The basic pulse time is programmed by selection of external resistance and capacitance values (see typical application data). The '122 and 'LS122 have internal timing resistors that allow the circuits to be used with only an external capacitor, if so desired. Once triggered, the basic pulse duration may be extended by retriggering the gated low-level-active (A) or high-level-active (B) inputs, or be reduced by use of the overriding clear. Figure 1 illustrates pulse control by retriggering and early clear.

The 'LS122 and 'LS123 are provided enough Schmitt hysteresis to ensure jitter-free triggering from the B input with transition rates as slow as 0.1 millivolt per nanosecond.

The Rint is nominally 10 k for '122 and 'LS122.



## **Recommended For You**

SN74S38N Texas Instruments, Inc DIP

SN74F08D Texas Instruments, Inc SOP-14

#### SN74LS245DW

Texas Instruments, Inc SOP20

#### **SN7406N**

Texas Instruments, Inc DIP-14

SN74LS14N Texas Instruments, Inc DIP

# SN7438N Texas Instruments, Inc DIP14

SN74LS257BN Texas Instruments, Inc DIP16

SN74LS74AN Texas Instruments, Inc DIP

SN74CBILV3257D Texas Instruments, Inc SOP-16P

SN74HC139N Texas Instruments, Inc DIP SN75462P

Texas Instruments, Inc DIP8

#### SN75452BP

Texas Instruments, Inc DIP8

SN74S74N Texas Instruments, Inc DIP

#### SN74HC138DR

Texas Instruments, Inc SOP16

SN74AVC16T245DGGR

Texas Instruments, Inc TSSOP48