

**CPLD MAX® 7000S Family 1.25K Gates 64 Macro Cells 100MHz  
5V 100-Pin TQFP Tray**



Images are for reference only

<b>Manufacturer:</b>	<a href="#">Intel Corp</a>
<b>Package/Case:</b>	QFP
<b>Product Type:</b>	Programmable Logic ICs
<b>Lifecycle:</b>	Obsolete

[Inquiry](#)

## General Description

The MAX 7000 family of high-density, high-performance PLDs is based on Altera's second-generation MAX architecture. Fabricated with advanced CMOS technology, the EEPROM-based MAX 7000 family provides 600 to 5,000 usable gates, ISP, pin-to-pin delays as fast as 5 ns, and counter speeds of up to 175.4 MHz. MAX 7000S devices in the -5, -6, -7, and -10 speed grades as well as MAX 7000 and MAX 7000E devices in -5, -6, -7, -10P, and -12P speed grades comply with the PCI Special Interest

## Recommended For You

### EPMB256AQC208-10N

Intel Corp  
QFP208

### EPCQ32ASI8N

Intel Corp  
SOP8

### EPCQ32SI8N

Intel Corp  
SOP8

### EPCQ64ASI16N

Intel Corp  
SOP16

### EPCQ16SI8N

Intel Corp  
SOP8

### EPC21I32

Intel Corp  
QFP

### EPM7128STC100-15N

Intel Corp  
QFP100

### EP1C6Q240I7N

Intel Corp  
QFP240

### EPCQ128SI16N

Intel Corp  
SOP16

### EPM7128SLC84-15N

Intel Corp  
PLCC

### EPC1213PC8

Intel Corp  
DIP8

### EP1K30TC144-3N

Intel Corp  
QFP

**EPCS1S18**

Intel Corp

SOP-8

**EPC1P18N**

Intel Corp

DIP8

**EPC2L120N**

Intel Corp

PLCC