

CPLD MAX® 7000 Family 3.75K Gates 192 Macro Cells 62.5MHz  
CMOS Technology 5V 160-Pin CPGA Tray

[EPM7192EGI160-20 Image](#)

Images are for reference only

<b>Manufacturer:</b>	<a href="#">Intel Corp</a>
<b>Package/Case:</b>	PGA
<b>Product Type:</b>	Programmable Logic ICs
<b>Lifecycle:</b>	Obsolete

[Inquiry](#)

## General Description

EPM7192EGI160-20 is a specific model of a Field-Programmable Gate Array (FPGA) produced by Intel. Here are some of its features:

## Key Features

Device Family: MAX II

Number of Logic Elements/Cells: 1,792

Total RAM Bits: 131,072

Number of I/Os: 127

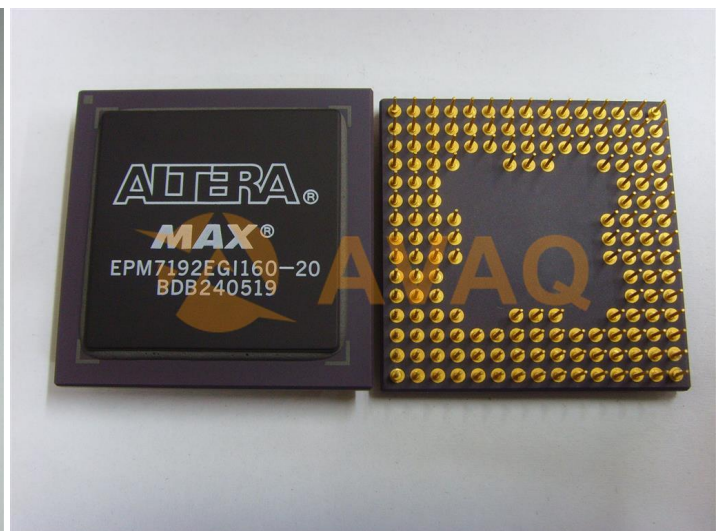
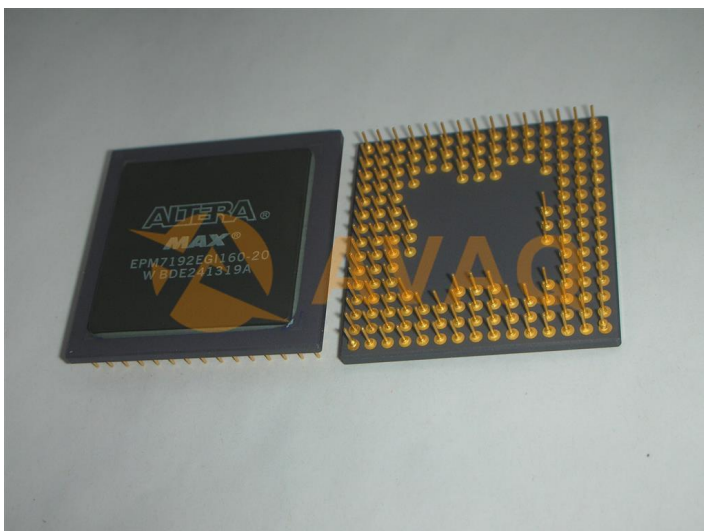
Operating Frequency: 200 MHz

Operating Voltage: 1.8V

Package Type: PGA

Package / Case: 160-LFBGA

Mounting Type: Surface Mount



## Recommended For You

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### EPMB256AQC208-10N

Intel Corp

QFP208

### EPCQ32ASI8N

Intel Corp

SOP8

### EPCQ32SI8N

Intel Corp

SOP8

### EPCQ64ASI16N

Intel Corp

SOP16

### EPCQ16SI8N

Intel Corp

SOP8

### EPC2II32

Intel Corp

QFP

### EPM7128STC100-15N

Intel Corp

QFP100

### EP1C6Q240I7N

Intel Corp

QFP240

### EPCQ128SI16N

Intel Corp

SOP16

### EPM7128SLC84-15N

Intel Corp

PLCC

### EPC1213PC8

Intel Corp

DIP8

### EP1K30TC144-3N

Intel Corp

QFP

### EPCS1SI8

Intel Corp

SOP-8

### EPC1PI8N

Intel Corp

DIP8

### EPC2LI20N

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