

FPGA APEX 20K Family 200K Gates 8320 Cells 200MHz 0.22um  
Technology 2.5V 240-Pin RQFP



Images are for reference only

<b>Manufacturer:</b>	<a href="#">Intel Corp</a>
<b>Package/Case:</b>	QFP
<b>Product Type:</b>	Programmable Logic ICs
<b>Lifecycle:</b>	Obsolete

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## General Description

APEX™ 20K devices are the first PLDs designed with the MultiCore architecture, which combines the strengths of LUT-based and product-term-based devices with an enhanced memory structure. LUT-based logic provides optimized performance and efficiency for data-path, register-intensive, mathematical, or digital signal processing (DSP) designs. Product-term-based logic is optimized for complex combinatorial paths, such as complex state machines. LUT- and product-term-based logic combined with memory functions and a wide variety of MegaCore and AMPP functions make the APEX 20K device architecture uniquely suited for system-on-a-programmable-chip designs. Applications historically requiring a combination of LUT-, product-term-, and memory-based devices can now be integrated into one APEX 20K device.



## Recommended For You

**EPMB256AQC208-10N**

Intel Corp

QFP208

**EPCQ32ASI8N**

Intel Corp

SOP8

**EPCQ32SI8N**

Intel Corp

SOP8

**EPCQ64ASI16N**

Intel Corp

SOP16

**EPCQ16SI8N**

Intel Corp

SOP8

**EPC2H32**

Intel Corp

QFP

**EPM7128STC100-15N**

Intel Corp

QFP100

**EP1C6Q240I7N**

Intel Corp

QFP240

**EPCQ128SI16N**

Intel Corp

SOP16

**EPM7128SLC84-15N**

Intel Corp

PLCC

**EPC1213PC8**

Intel Corp

DIP8

**EP1K30TC144-3N**

Intel Corp

QFP

**EPCS1SI8**

Intel Corp

SOP-8

**EPC1PI8N**

Intel Corp

DIP8

**EPC2LI20N**

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